

FIG. 1
(PRIOR ART)

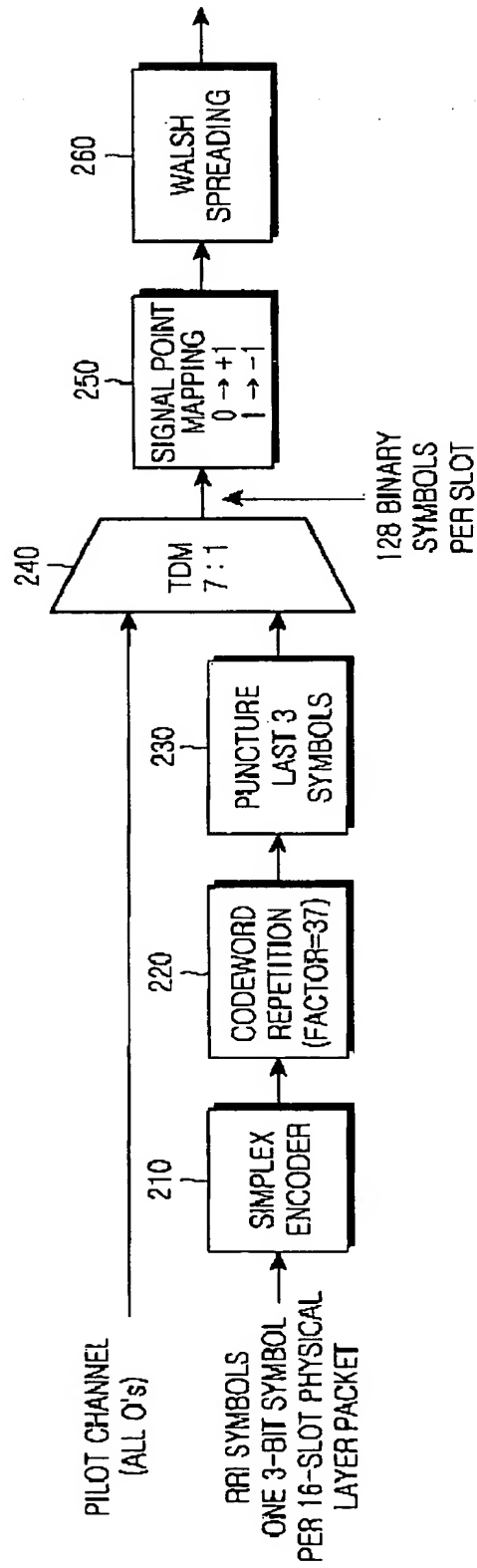


FIG. 2
(PRIOR ART)

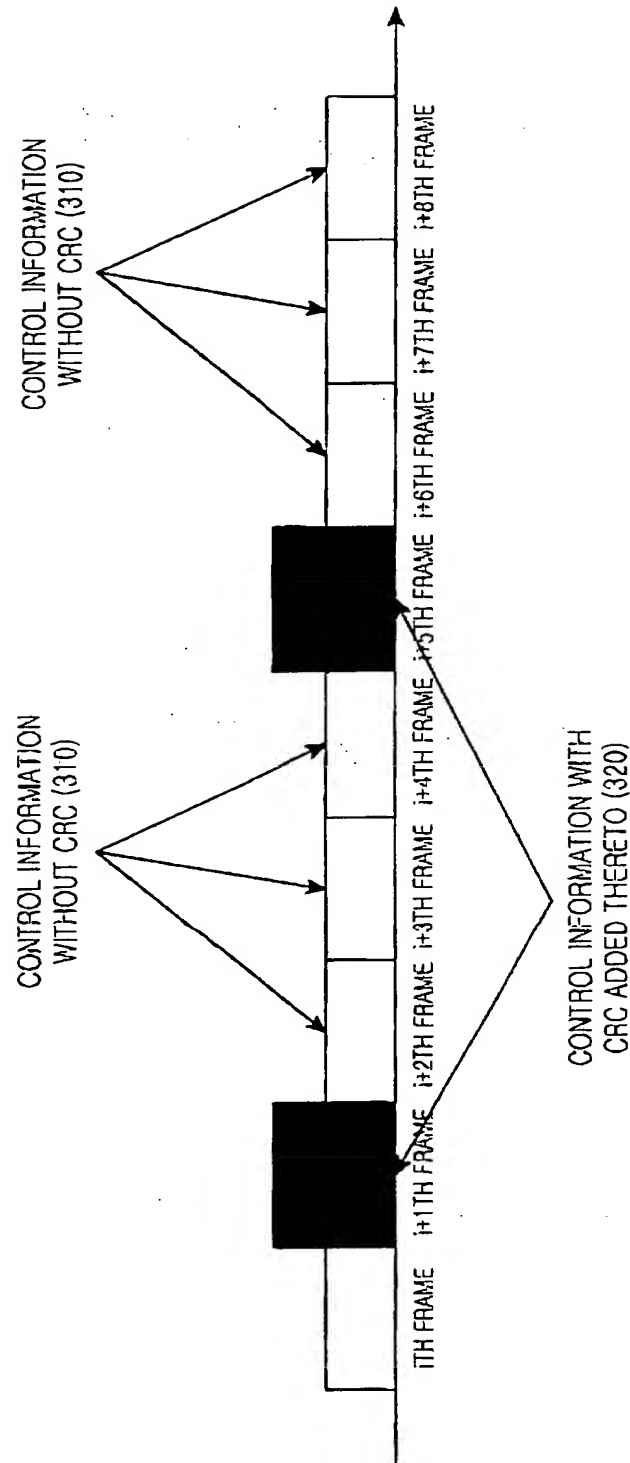


FIG.3

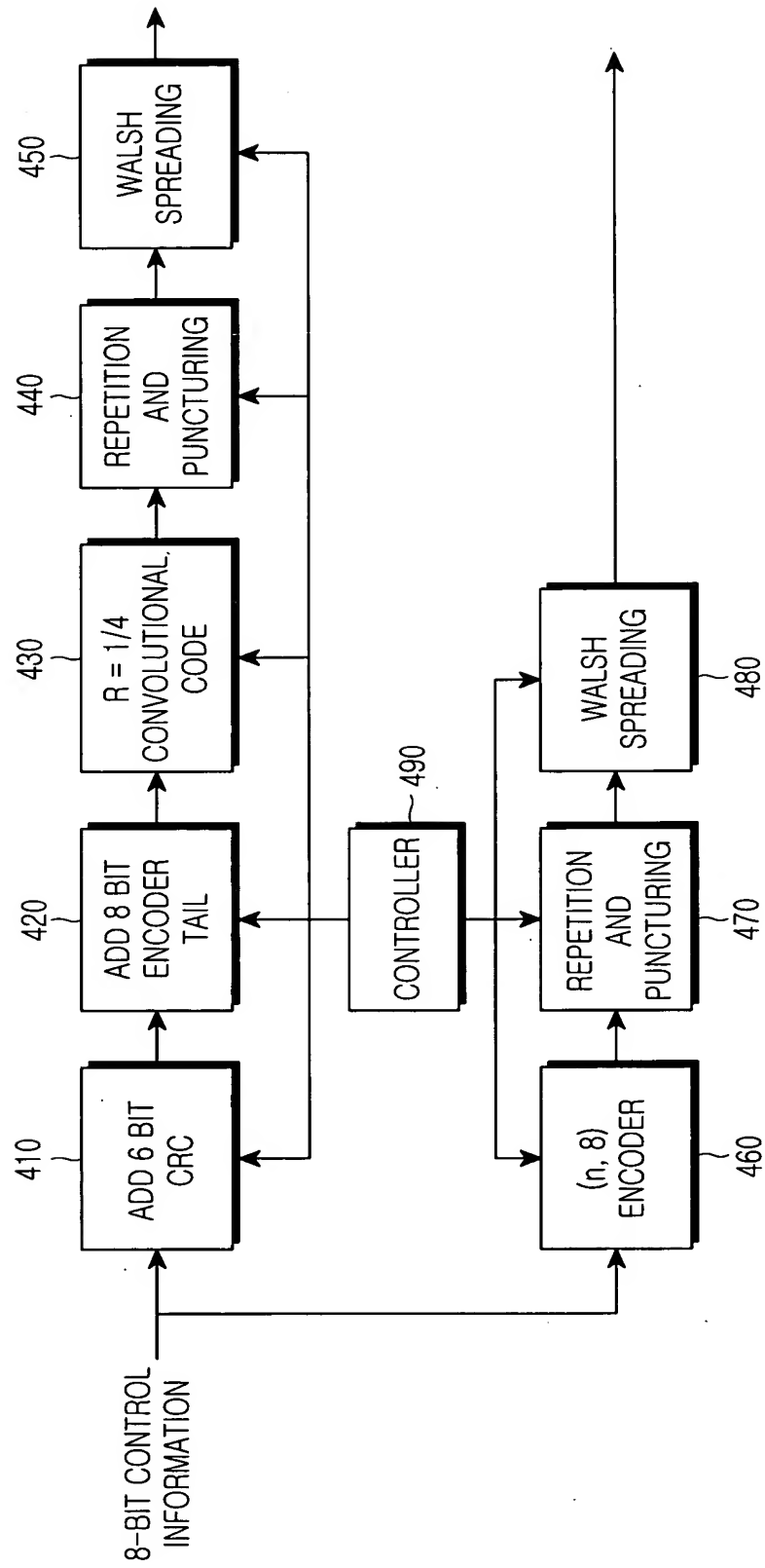


FIG. 4

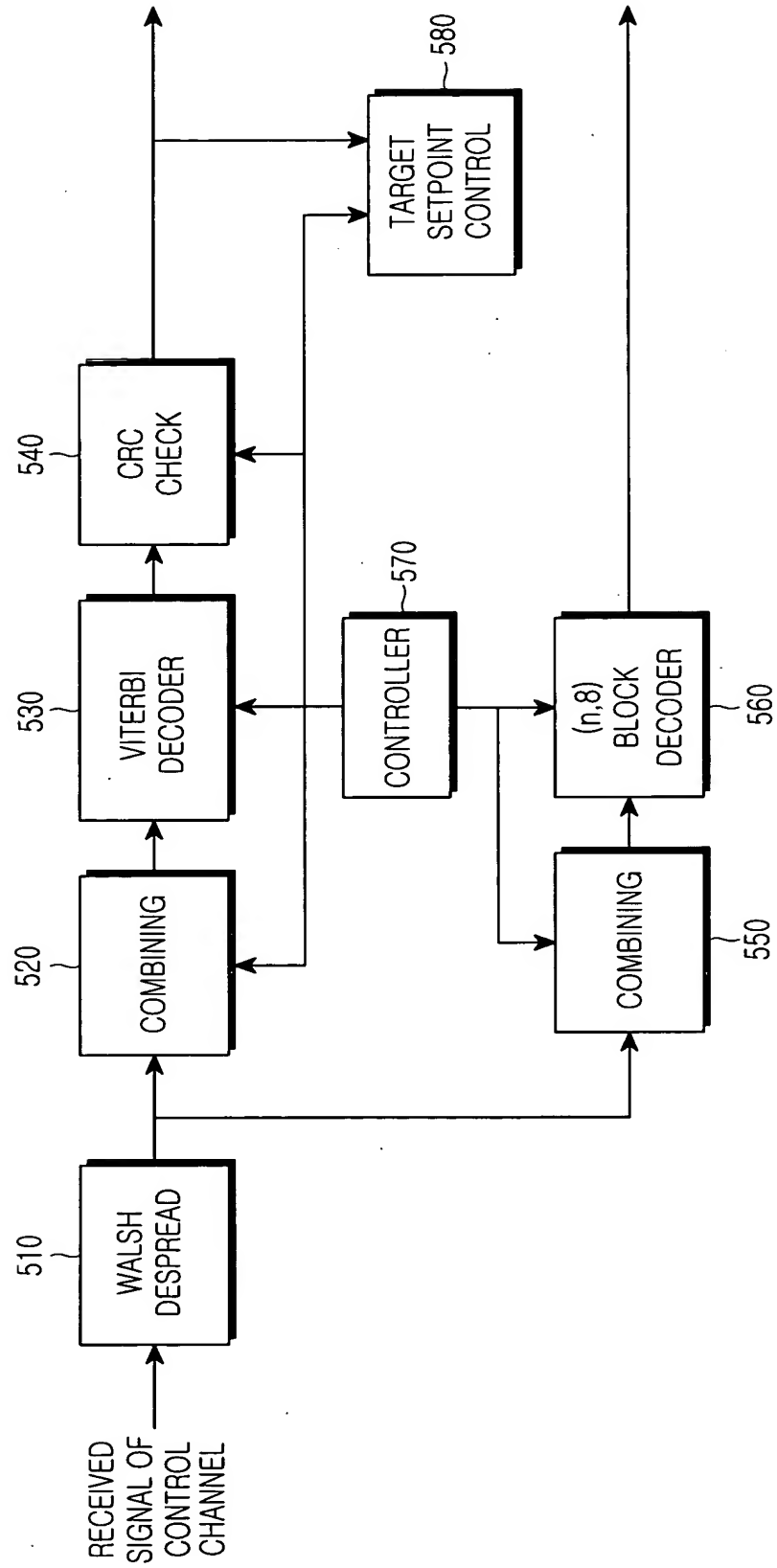


FIG.5

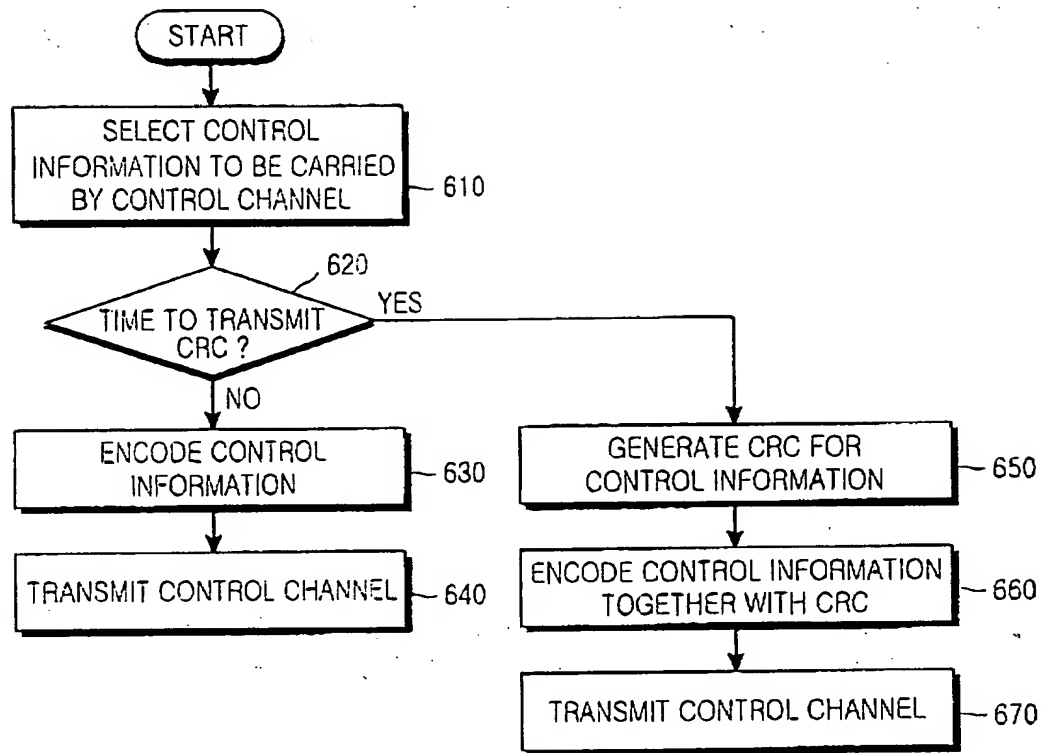


FIG. 6

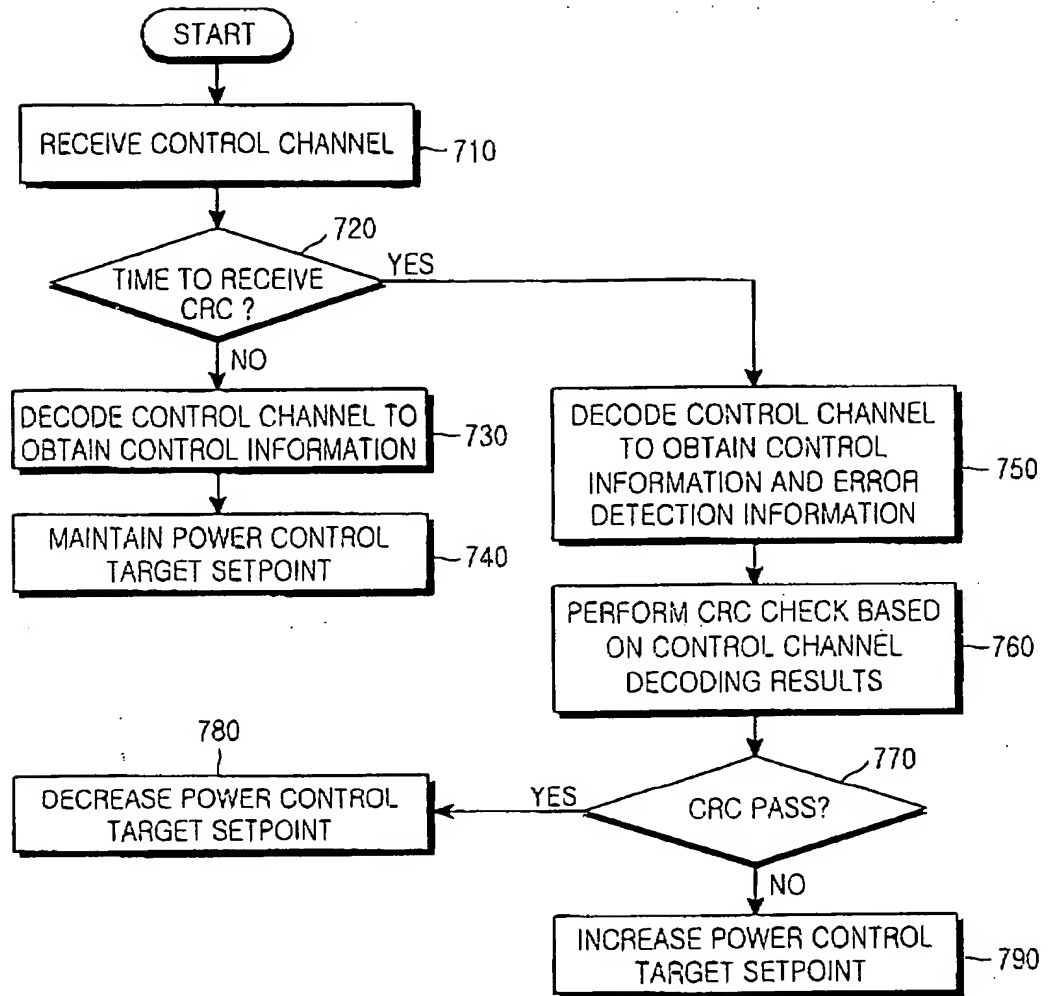


FIG. 7

8-BIT CRC IS TRANSMITTED WHILE BEING EQUALLY
DIVIDED INTO FOUR 2-BIT PARTS

CONTROL CHANNEL: 8-BIT CONTROL INFORMATION, 2 BITS OF 8-BIT CRC	CONTROL CHANNEL: 8-BIT CONTROL INFORMATION, 2 BITS OF 8-BIT CRC	CONTROL CHANNEL: 8-BIT CONTROL INFORMATION, 2 BITS OF 8-BIT CRC	CONTROL CHANNEL: 8-BIT CONTROL INFORMATION, 2 BITS OF 8-BIT CRC	CONTROL CHANNEL: 8-BIT CONTROL INFORMATION, 2 BITS OF 8-BIT CRC	CONTROL CHANNEL: 8-BIT CONTROL INFORMATION, 2 BITS OF 8-BIT CRC	CONTROL CHANNEL: 8-BIT CONTROL INFORMATION, 2 BITS OF 8-BIT CRC	CONTROL CHANNEL: 8-BIT CONTROL INFORMATION, 2 BITS OF 8-BIT CRC
i-3th FRAME	i-2th FRAME	i-1th FRAME	ith FRAME	i+1th FRAME	i+2th FRAME	i+3th FRAME	i+4th FRAME

FIG.8

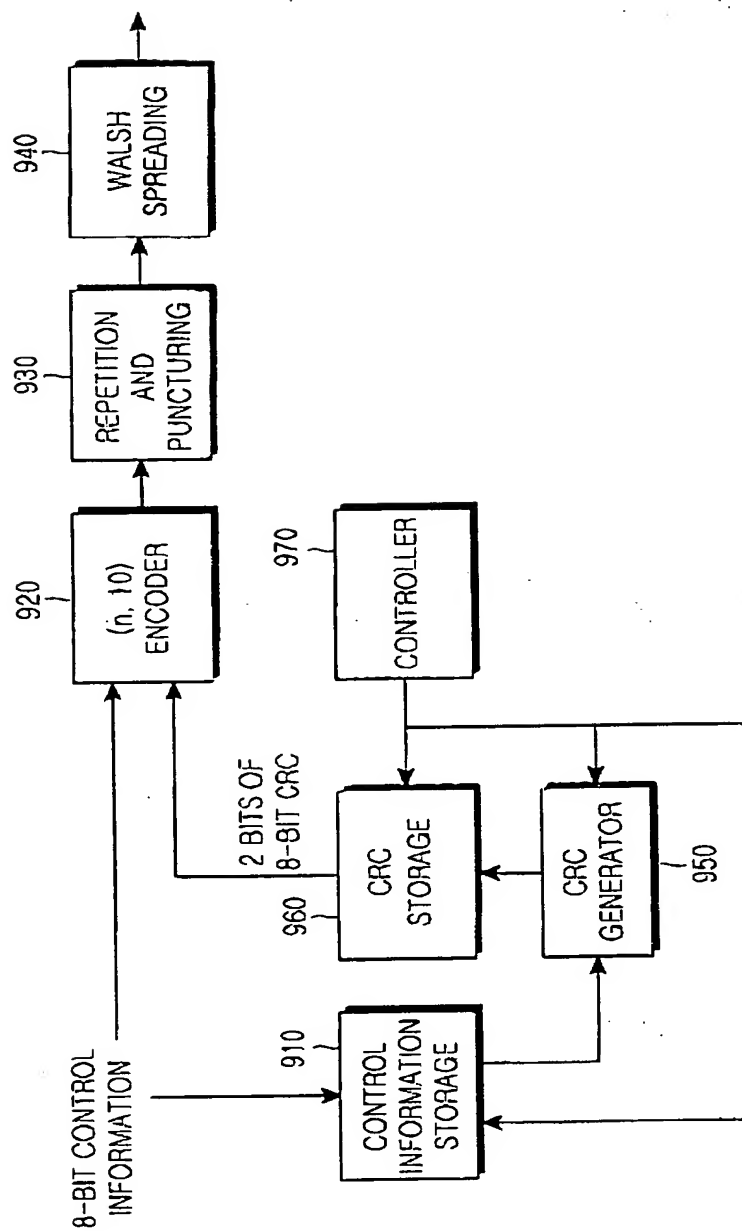


FIG.9

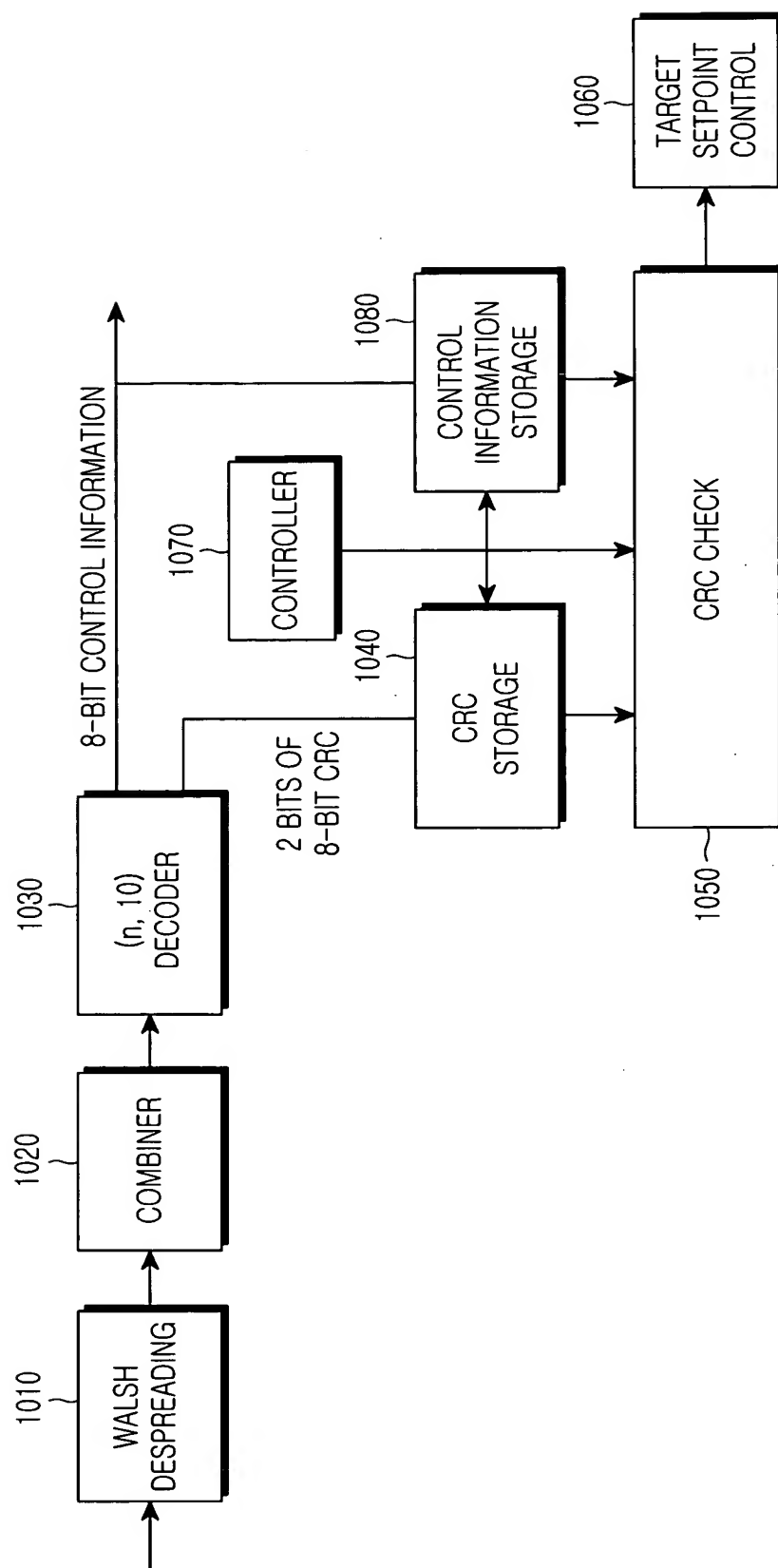


FIG.10

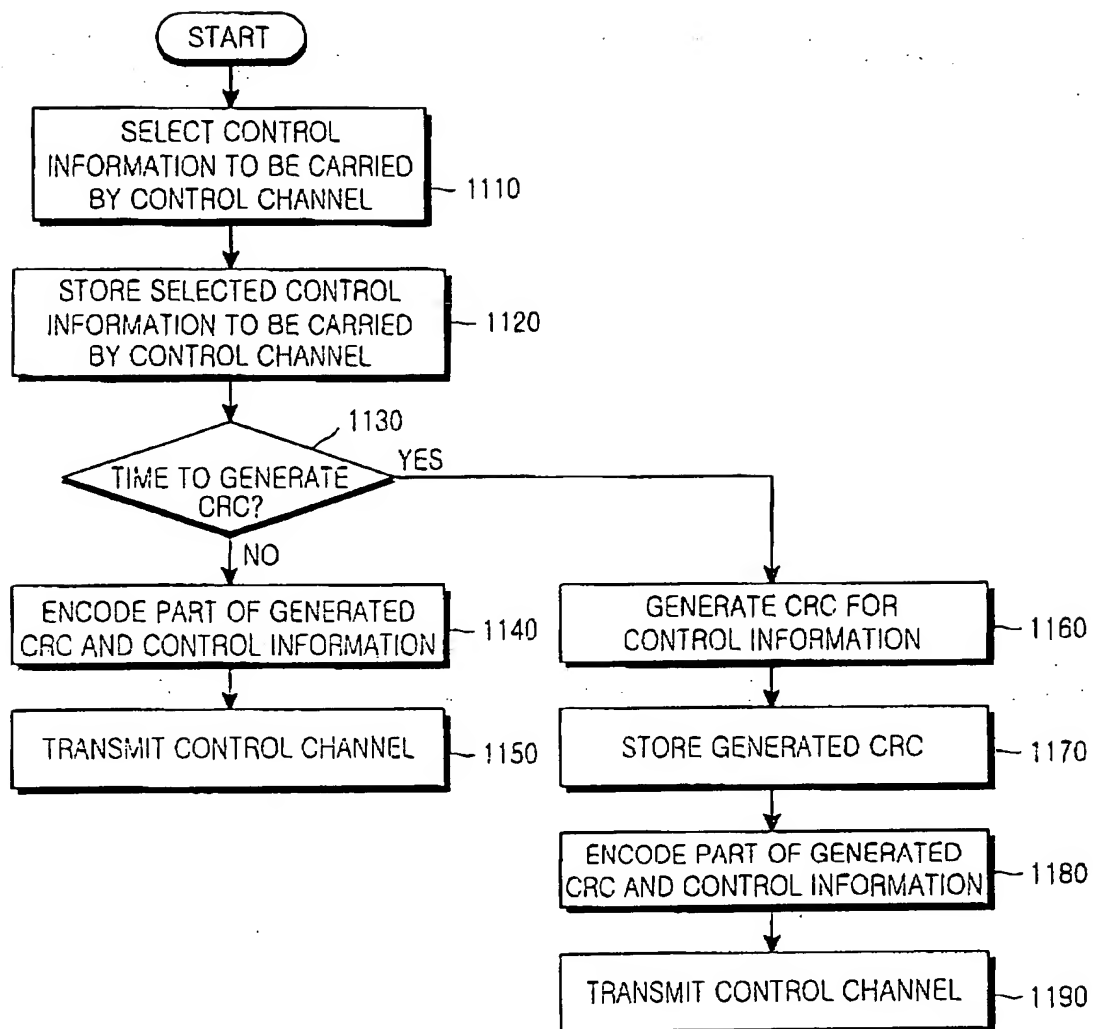


FIG. 11

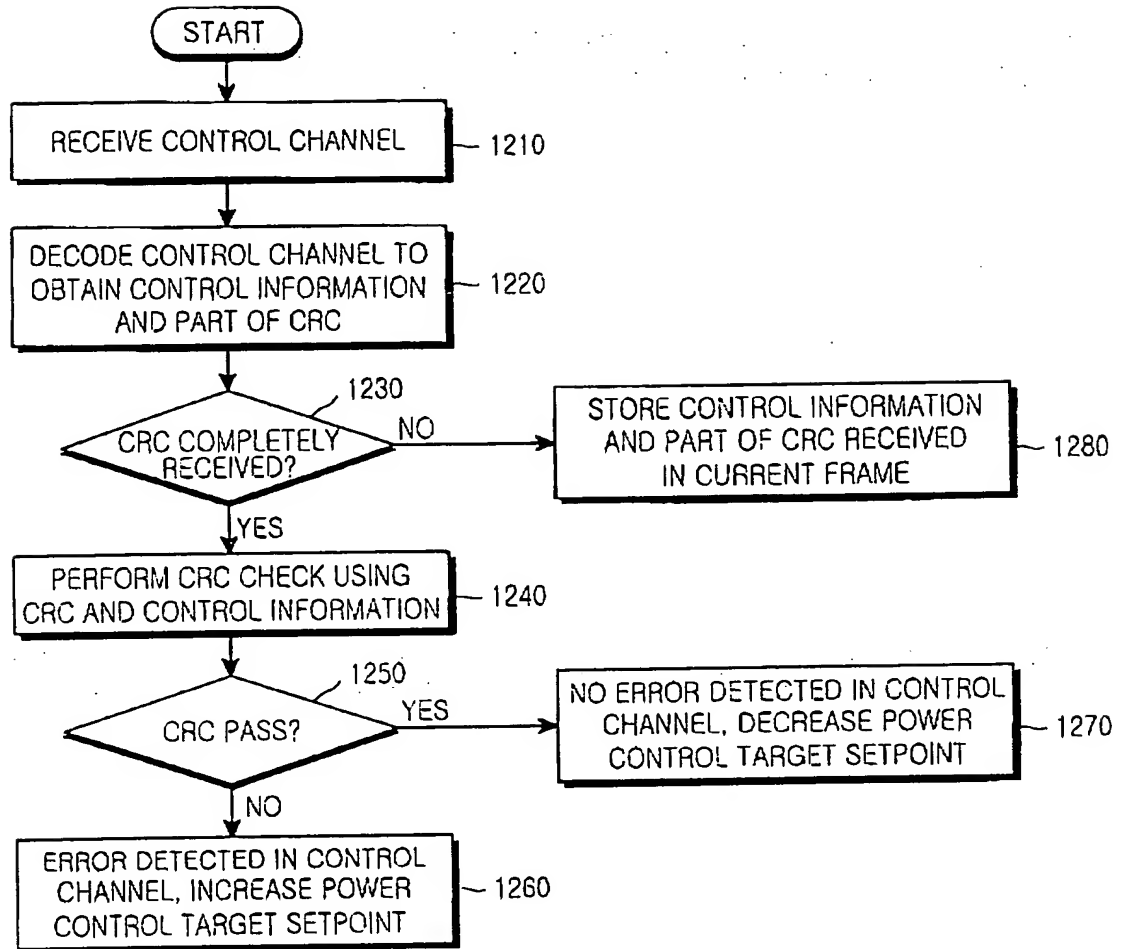


FIG.12

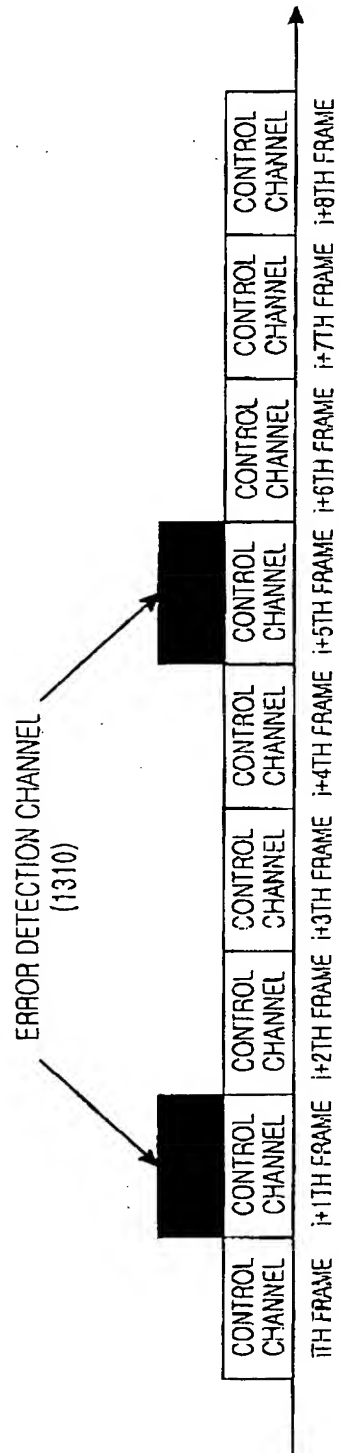


FIG.13

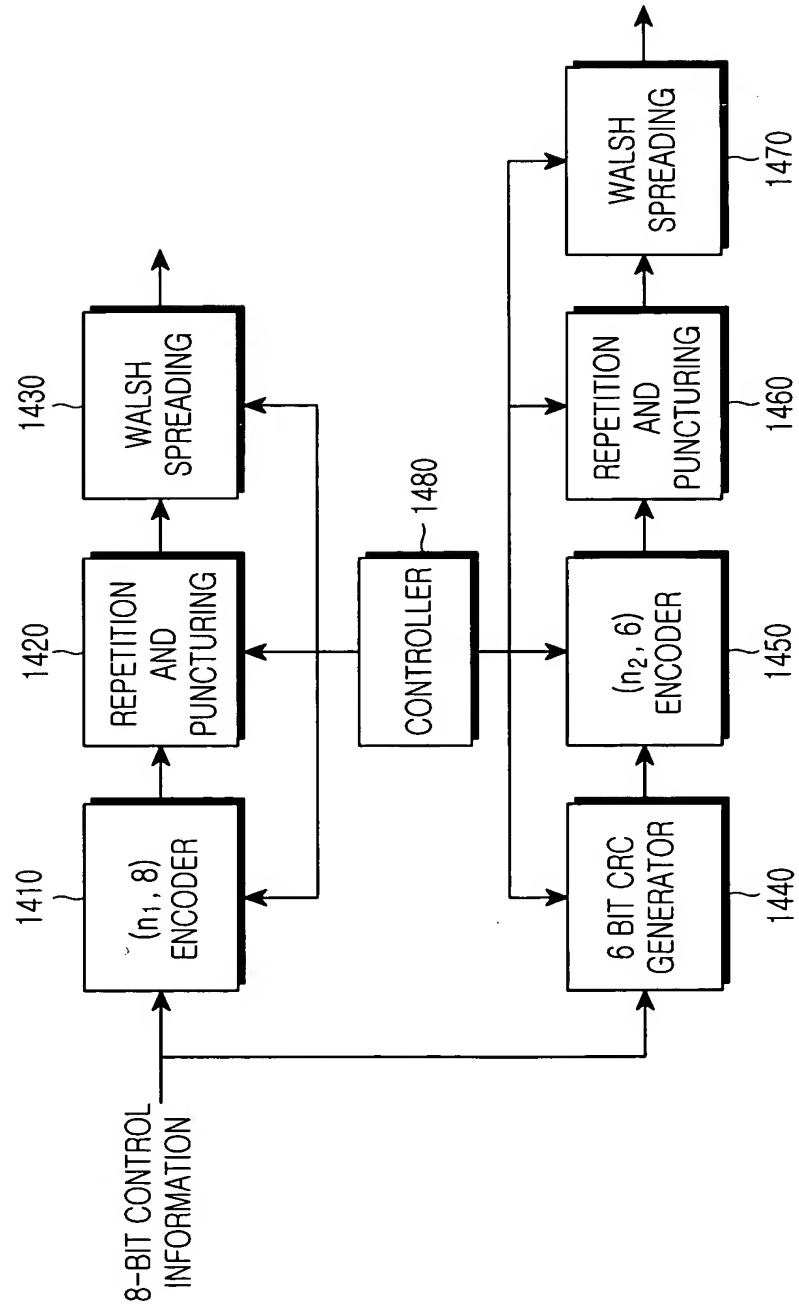


FIG.14

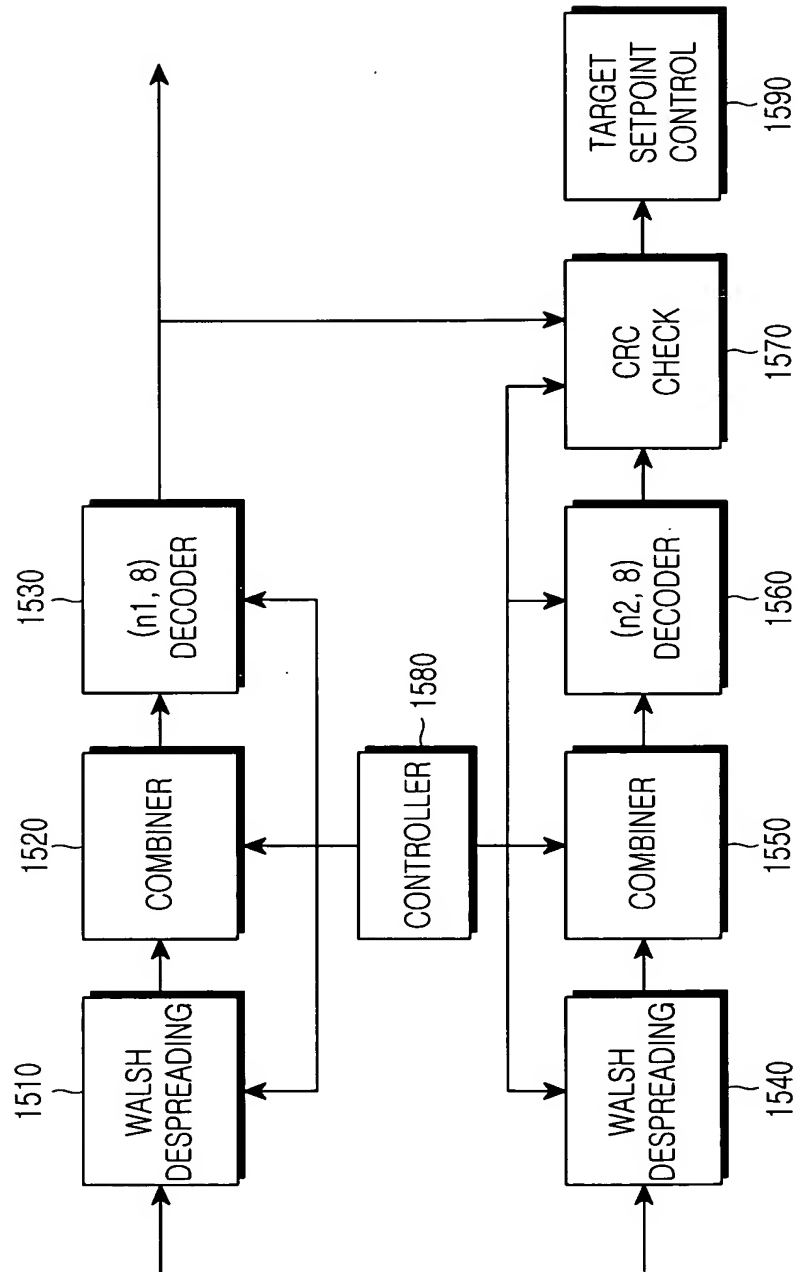


FIG.15

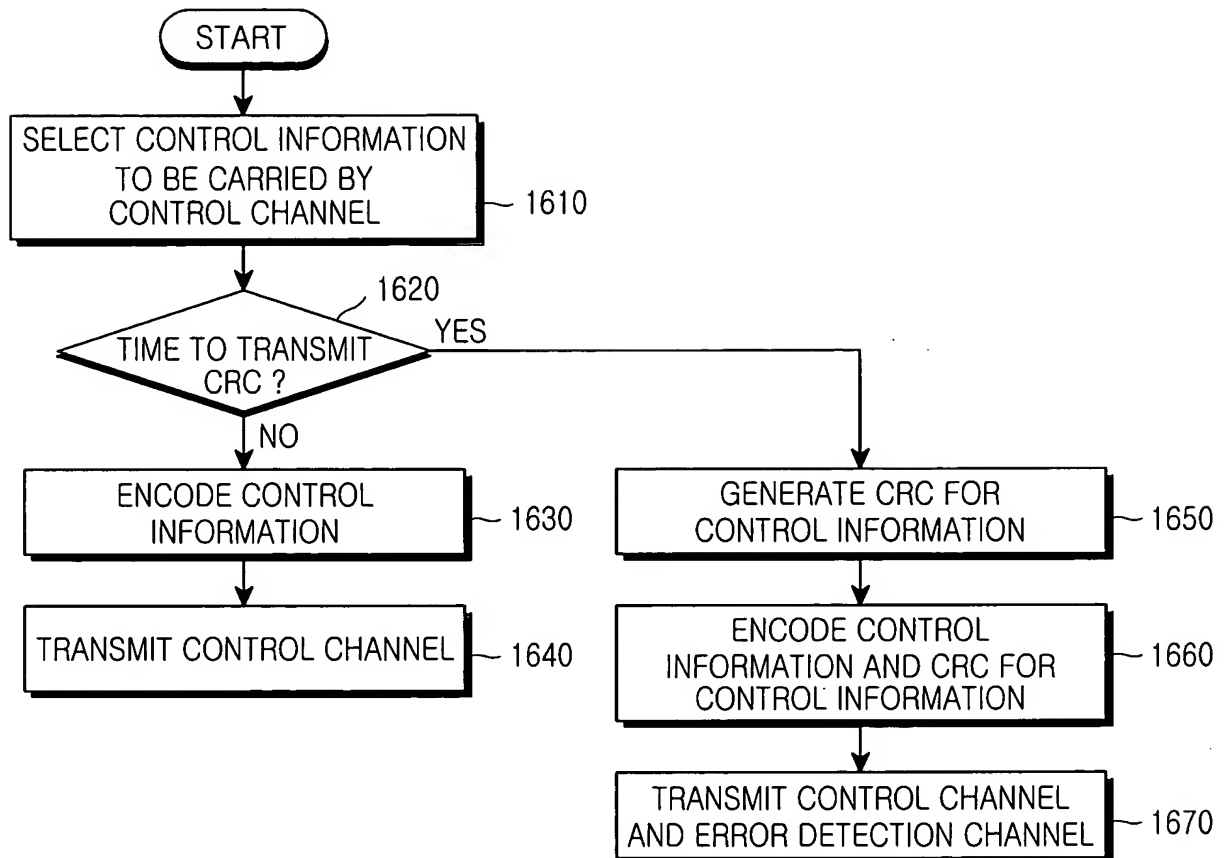


FIG.16

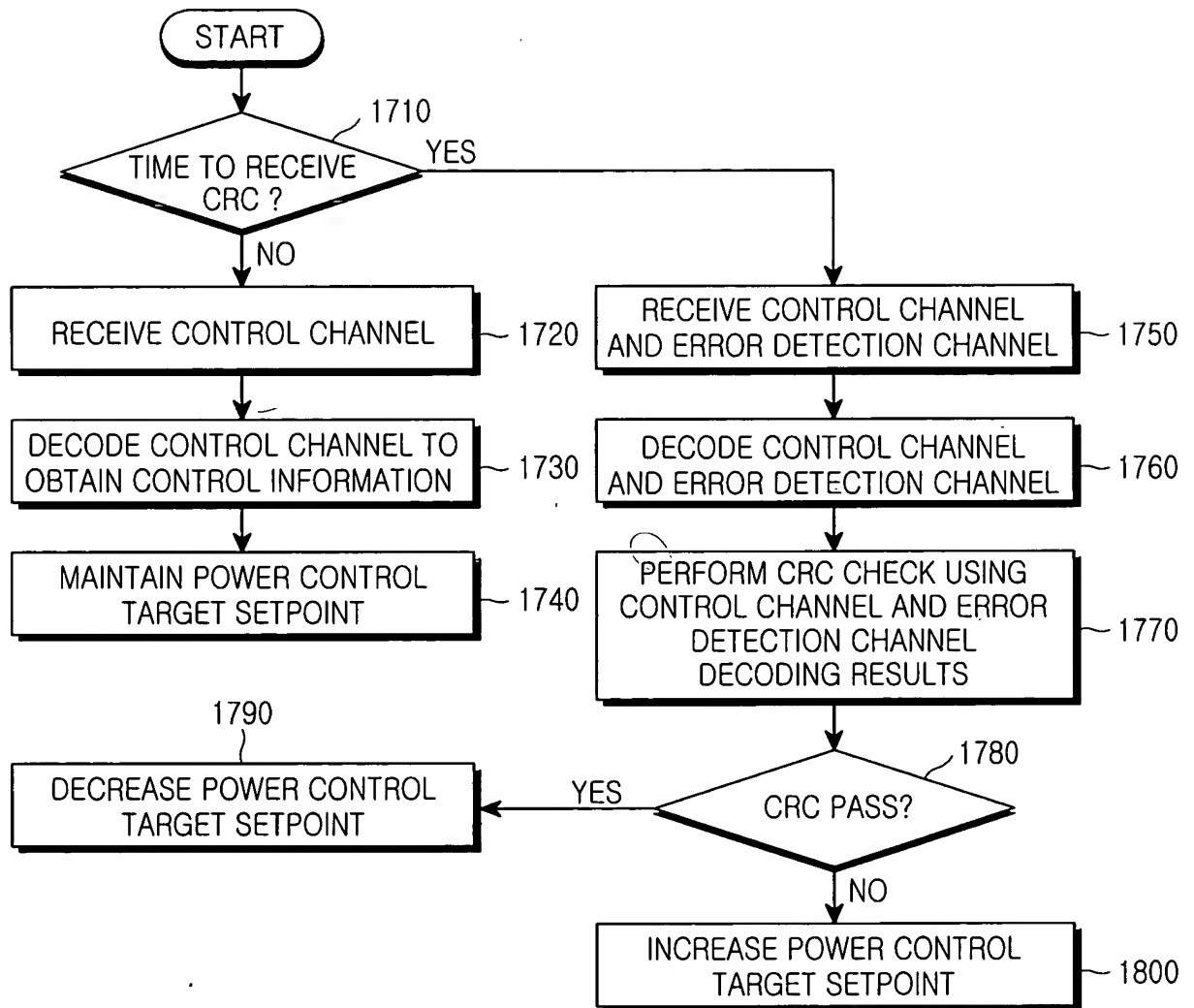


FIG.17

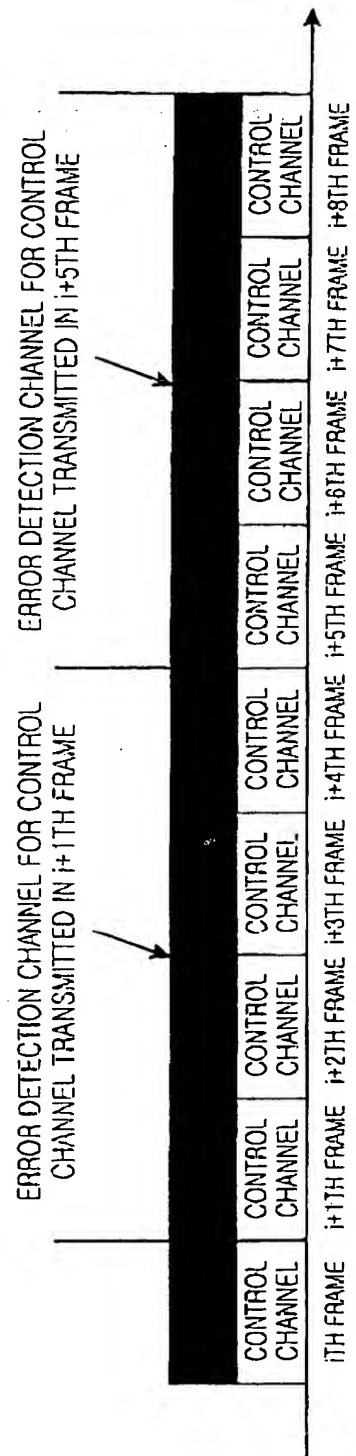


FIG.18

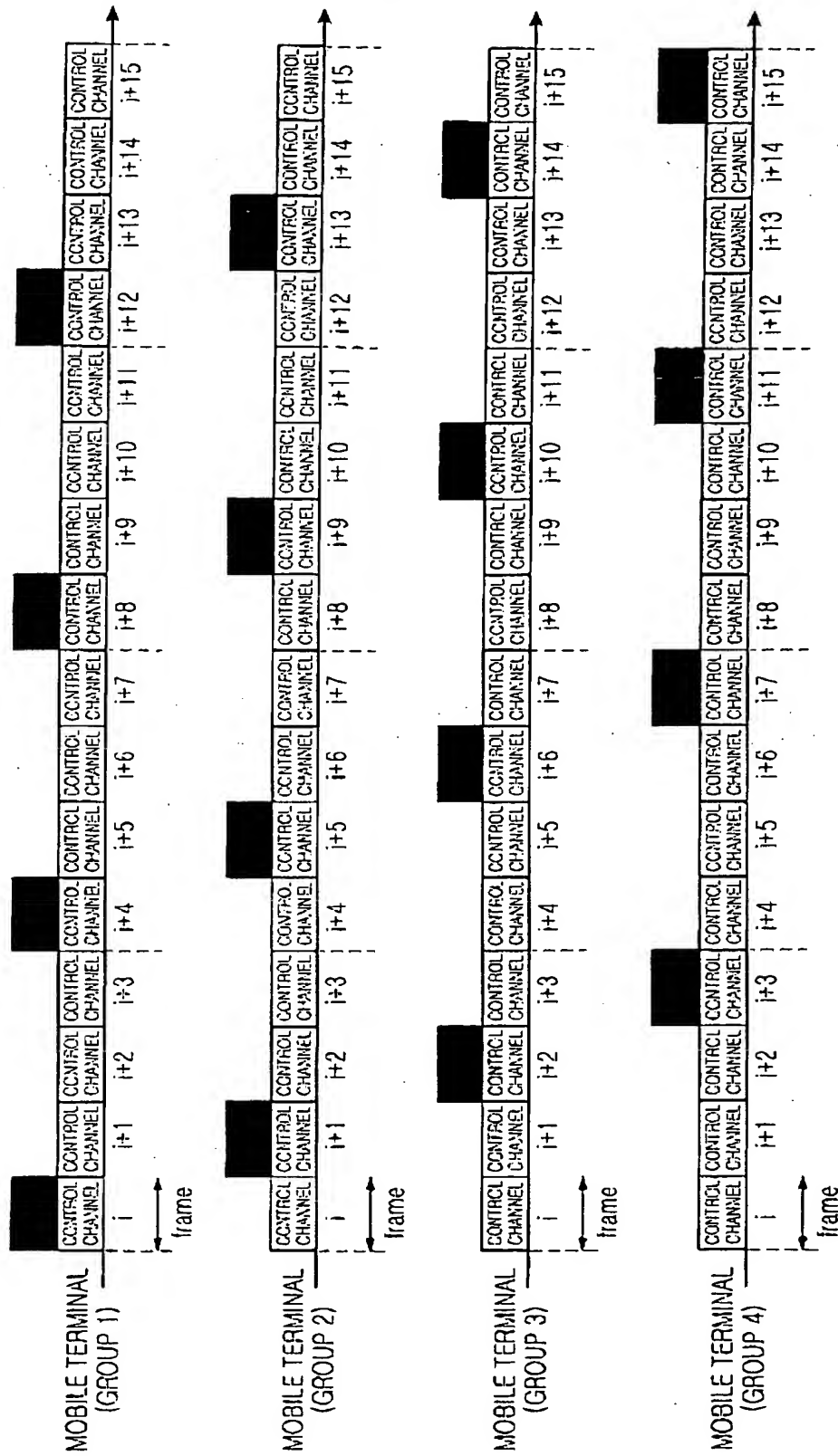


FIG.19

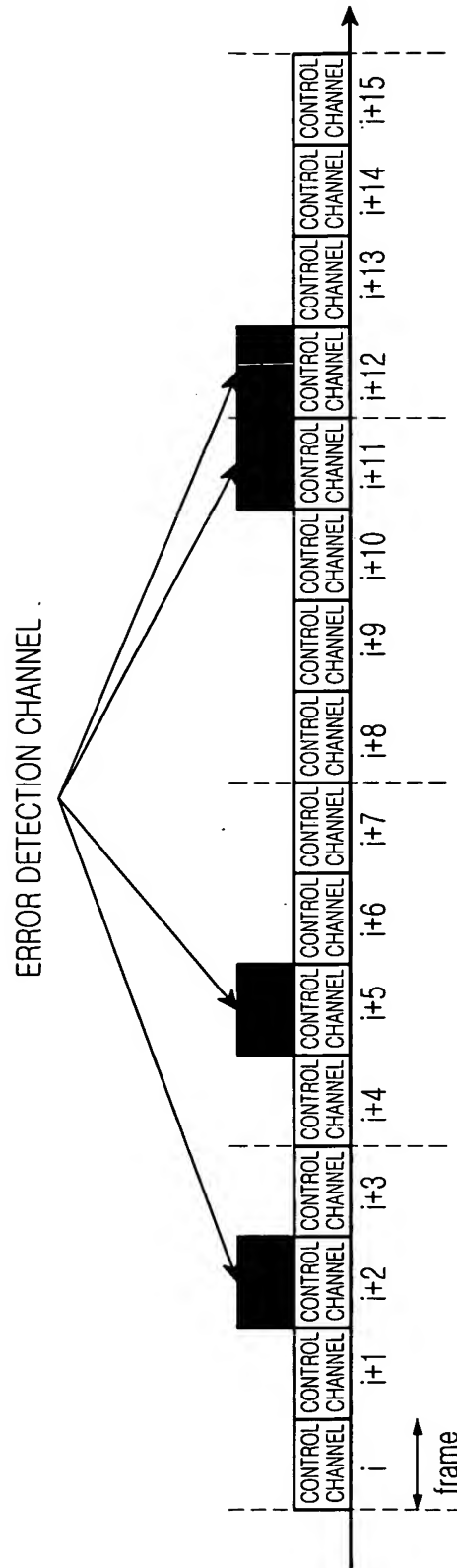


FIG.20

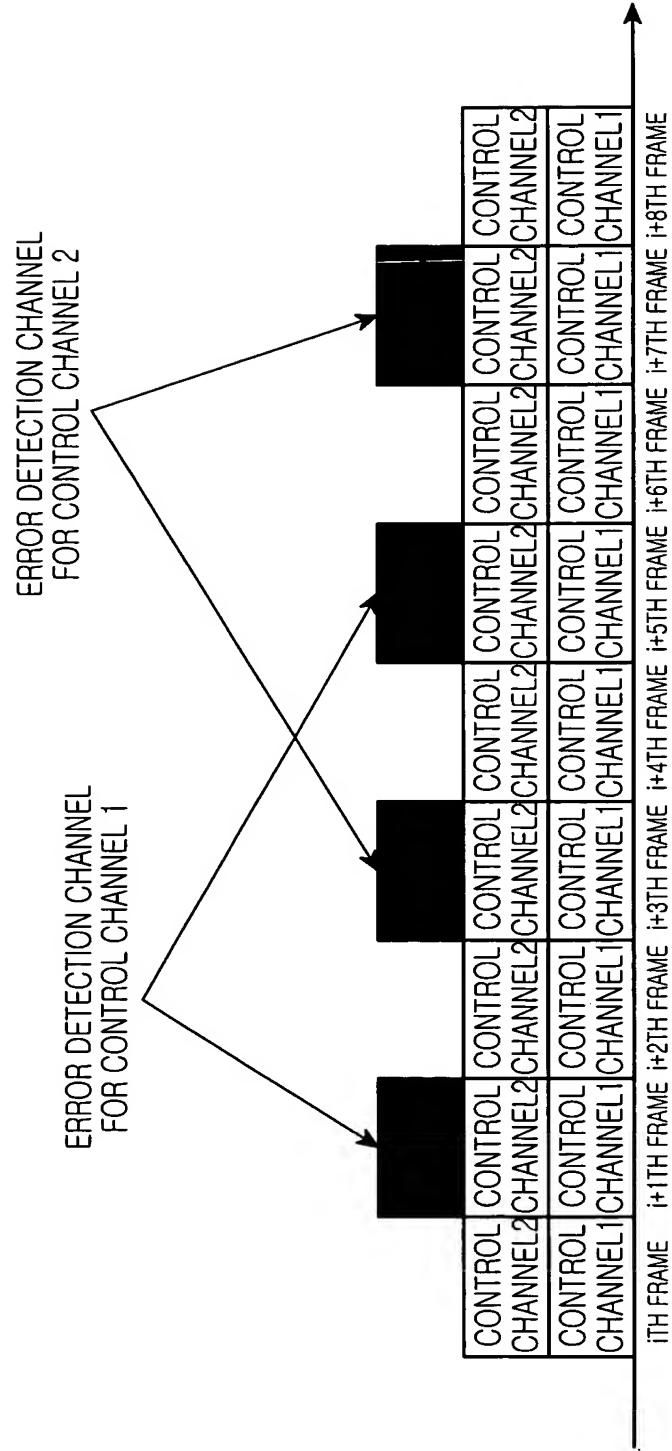


FIG. 21